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### Search Results -

Term	Documents
STORE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	430873
STORES.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	445867
BUFFER.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	490861
BUFFERS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	124648
ENTRIES.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	56804
ENTRY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	349738
ENTRYS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	62
MULTIPLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	973307
MULTIPLES.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	31917
MULTI.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	661615
MULTIS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	42
(STORE NEAR1 BUFFER NEAR5 ENTRIES AND (MULTIPLE OR MULTI OR PLURAL\$5) NEAR5 MATCH AND LOAD).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	13

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		Search History	

DATE: Monday, September 09, 2002 Printable Copy Create Case

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**Search Results -** Record(s) 1 through 3 of 3 returned.

☐ 1. Document ID: US 6223320 B1

L5: Entry 1 of 3

File: USPT

Apr 24, 2001

US-PAT-NO: 6223320

DOCUMENT-IDENTIFIER: US 6223320 B1

TITLE: Efficient CRC generation utilizing parallel table lookup operations



2. Document ID: US 5887183 A

L5: Entry 2 of 3

File: USPT

Mar 23, 1999

US-PAT-NO: 5887183

DOCUMENT-IDENTIFIER: US 5887183 A

TITLE: Method and system in a data processing system for loading and storing vectors

in a plurality of modes



☐ 3. Document ID: US 5764997 A

L5: Entry 3 of 3

File: USPT

Jun 9, 1998

US-PAT-NO: 5764997

DOCUMENT-IDENTIFIER: US 5764997 A

TITLE: System for generating interrupt requests from either side of an inter-chip

bus



**Generate Collection** 

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Term	Documents
TARGET.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	414203
TARGETS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	63779
VECTOR\$	0
VECTOR.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	199003
VECTORA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	21
VECTORAB.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
VECTORABILITY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
VECTORABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	193
VECTORADD.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	6
VECTORADDS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
"VECTORADD[".DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	3
(L3 AND TARGET NEAR1 VECTOR\$).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	3

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Previous Page Next Page

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Search Results - Record(s) 1 through 20 of 39 returned.

☐ 1. Document ID: US 20020116595 A1

L6: Entry 1 of 39

File: PGPB

Aug 22, 2002

PGPUB-DOCUMENT-NUMBER: 20020116595

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020116595 A1

TITLE: Digital signal processor integrated circuit

PUBLICATION-DATE: August 22, 2002

INVENTOR-INFORMATION:

NAME

CITY

STATE

COUNTRY

RULE-47

Morton, Steven G.

Oxford

CT

US

US-CL-CURRENT: 712/22

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Draw, Desc. Image

MORE

2. Document ID: US 6332186 B1

L6: Entry 2 of 39

File: USPT

Dec 18, 2001

US-PAT-NO: 6332186

DOCUMENT-IDENTIFIER: US 6332186 B1

TITLE: Vector register addressing

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Draw Desc Image

KWIC

☐ 3. Document ID: US 6317819 B1

L6: Entry 3 of 39

File: USPT

Nov 13, 2001

US-PAT-NO: 6317819

DOCUMENT-IDENTIFIER: US 6317819 B1

TITLE: Digital signal processor containing scalar processor and a plurality of

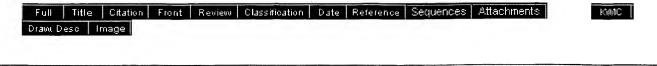
vector processors operating from a single instruction

Full Title Citation Front Review Classification Date Reference Sequences Attachments

KOMC

		, , , , , , , , , , , , , , , , , , , ,
☐ 4. Document ID: US 6304 L6: Entry 4 of 39	1963 Bl File: USPT	Oct 16, 2001
US-PAT-NO: 6304963 DOCUMENT-IDENTIFIER: US 630496	3 B1	
TITLE: Handling exceptions occ	uring during processing of	vector instructions
Full Title Citation Front Review  Draw. Desc   Image	Classification Date Reference Sequent	ces Attachments KMC
☐ 5. Document ID: US 6282	2634 B1	
L6: Entry 5 of 39	File: USPT	Aug 28, 2001
US-PAT-NO: 6282634 DOCUMENT-IDENTIFIER: US 628263	4 B1	
TITLE: Apparatus and method foregister file	r processing data having a	mixed vector/scalar
Full Title Citation Front Review  Drawn Desc   Image	Classification Date Reference Sequenc	ces Attachments KMC
☐ 6. Document ID: US 6266	5758 B1	
L6: Entry 6 of 39	File: USPT	Jul 24, 2001
US-PAT-NO: 6266758 DOCUMENT-IDENTIFIER: US 626675	8 B1	
TITLE: Alignment and ordering data processing	of vector elements for sing	le instruction multiple
Full Title Citation Front Review  Drawn Desc   Image	Classification Date Reference Sequen	ces Attachments KWIC
☐ 7. Document ID: US 624	7113 B1	
L6: Entry 7 of 39	File: USPT	Jun 12, 2001
US-PAT-NO: 6247113 DOCUMENT-IDENTIFIER: US 624711	3 B1	

TITLE: Coprocessor opcode division by data type



8. Document ID: US 6223320 B1

L6: Entry 8 of 39

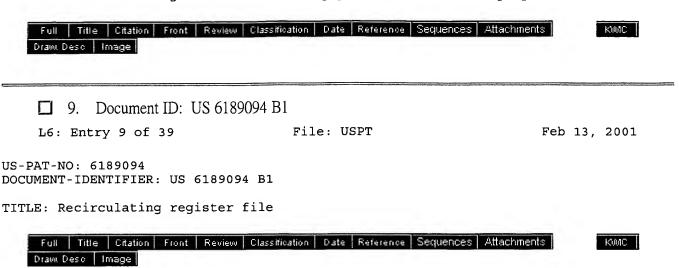
File: USPT

Apr 24, 2001

US-PAT-NO: 6223320

DOCUMENT-IDENTIFIER: US 6223320 B1

TITLE: Efficient CRC generation utilizing parallel table lookup operations



☐ 10. Document ID: US 6170001 B1

L6: Entry 10 of 39

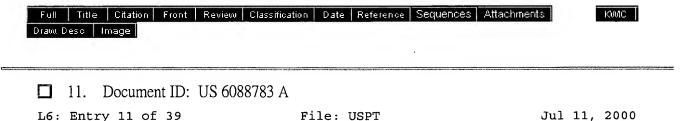
File: USPT

Jan 2, 2001

US-PAT-NO: 6170001

DOCUMENT-IDENTIFIER: US 6170001 B1

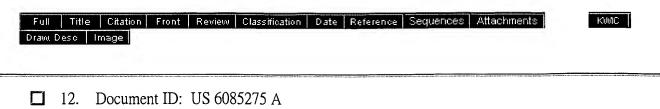
TITLE: System for transfering format data from format register to memory wherein format data indicating the distribution of single or double precision data type in the register bank



US-PAT-NO: 6088783

DOCUMENT-IDENTIFIER: US 6088783 A

TITLE: DPS having a plurality of like processors controlled in parallel by an instruction word, and a control processor also controlled by the instruction word



L6: Entry 12 of 39

File: USPT

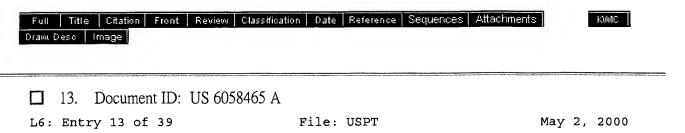
Jul 4, 2000

US-PAT-NO: 6085275

Dec 15, 1998

DOCUMENT-IDENTIFIER: US 6085275 A

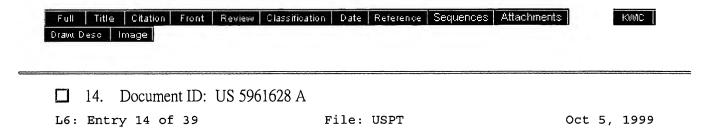
TITLE: Data processing system and method thereof



US-PAT-NO: 6058465

DOCUMENT-IDENTIFIER: US 6058465 A

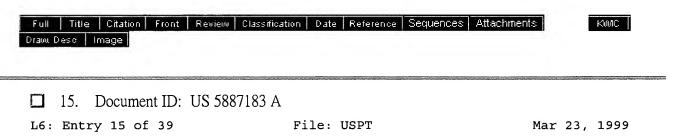
TITLE: Single-instruction-multiple-data processing in a multimedia signal processor



US-PAT-NO: 5961628

DOCUMENT-IDENTIFIER: US 5961628 A

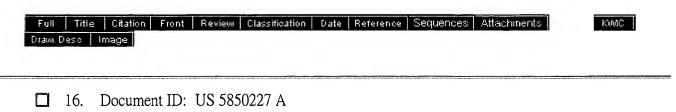
TITLE: Load and store unit for a vector processor



US-PAT-NO: 5887183

DOCUMENT-IDENTIFIER: US 5887183 A

TITLE: Method and system in a data processing system for loading and storing vectors in a plurality of modes



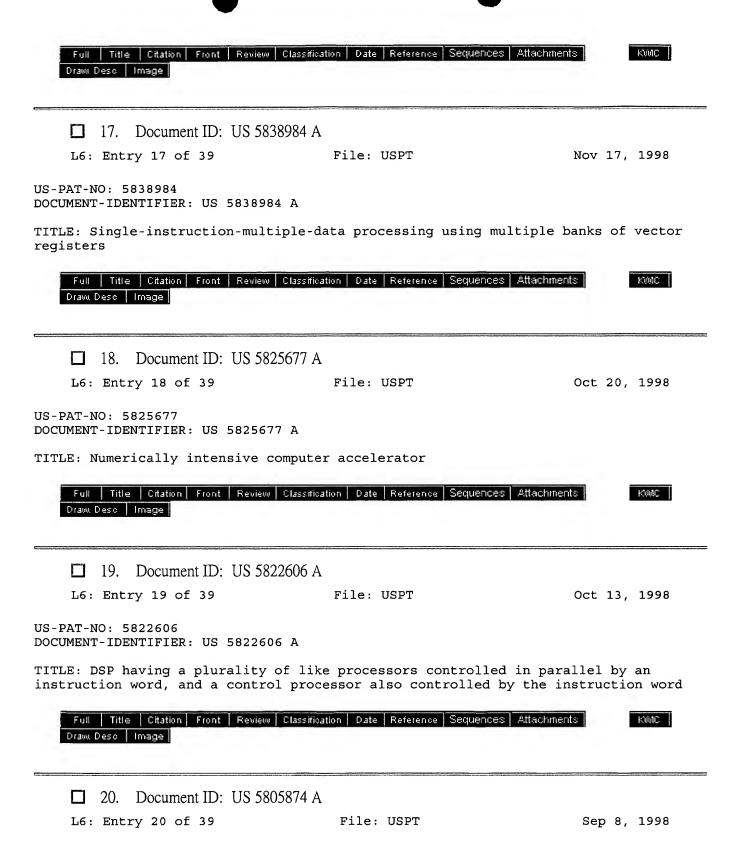
File: USPT

US-PAT-NO: 5850227

L6: Entry 16 of 39

DOCUMENT-IDENTIFIER: US 5850227 A

TITLE: Bit map stretching using operand routing and operation selective multimedia extension unit



US-PAT-NO: 5805874

DOCUMENT-IDENTIFIER: US 5805874 A

TITLE: Method and apparatus for performing a vector skip instruction in a data processor



KWIC

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Term	Documents
ALIGN\$6	0
ALIGN.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	227813
ALIGNA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	4
ALIGNABAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABEL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABIE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABILTY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13259
ALIGNABLITY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABLY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	248
(L4 AND ALIGN\$6).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	39

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Change Format

Previous Page

Next Page

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Search Results - Record(s) 21 through 39 of 39 returned.

☐ 21. Document ID: US 5799163 A

L6: Entry 21 of 39

File: USPT

Aug 25, 1998

KMAC

US-PAT-NO: 5799163

DOCUMENT-IDENTIFIER: US 5799163 A

TITLE: Opportunistic operand forwarding to minimize register file read ports

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Draw Desc Image

☐ 22. Document ID: US 5754805 A

L6: Entry 22 of 39

File: USPT

May 19, 1998

US-PAT-NO: 5754805

DOCUMENT-IDENTIFIER: US 5754805 A

TITLE: Instruction in a data processing system utilizing extension bits and method

therefor

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KMC |
Draw, Desc | Image |

☐ 23. Document ID: US 5752074 A

L6: Entry 23 of 39

File: USPT

May 12, 1998

US-PAT-NO: 5752074

DOCUMENT-IDENTIFIER: US 5752074 A

TITLE: Data processing system and method thereof

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Draws Desc Image

24. Document ID: US 5742786 A

L6: Entry 24 of 39

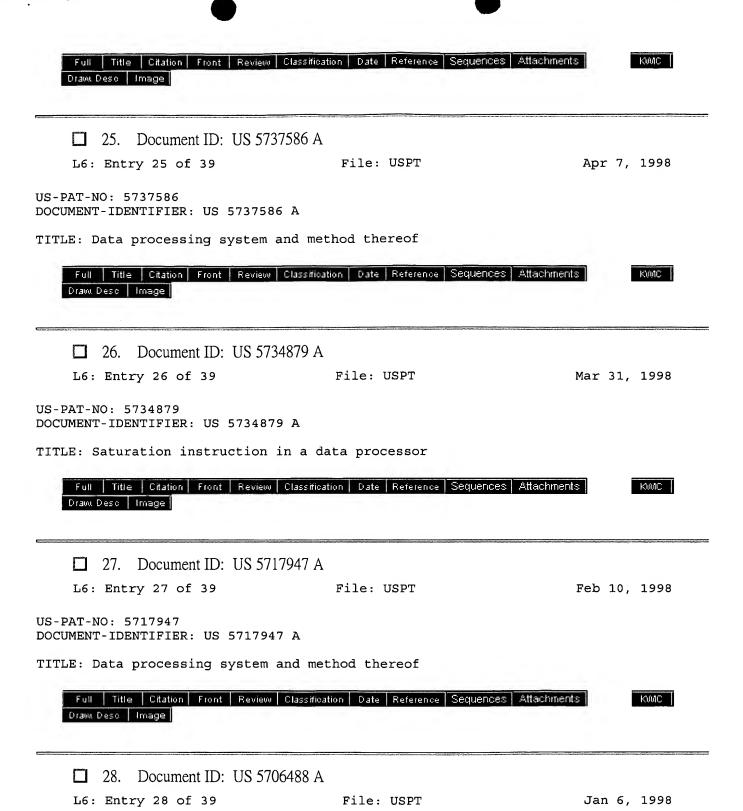
File: USPT

Apr 21, 1998

US-PAT-NO: 5742786

DOCUMENT-IDENTIFIER: US 5742786 A

TITLE: Method and apparatus for storing vector data in multiple non-consecutive locations in a data processor using a mask value



US-PAT-NO: 5706488

DOCUMENT-IDENTIFIER: US 5706488 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KOMC
Draw. D	esc II	nage								

☐ 29. Document ID: US 5673407 A
L6: Entry 29 of 39

File: USPT

Sep 30, 1997

US-PAT-NO: 5673407

DOCUMENT-IDENTIFIER: US 5673407 A

TITLE: Data processor having capability to perform both floating point operations and memory access in response to a single instruction

Full Title Citation Front Review Classification Date Reference Sequences Attachments

KWIC

Draw Desc | Image |

☐ 30. Document ID: US 5664134 A

L6: Entry 30 of 39

File: USPT

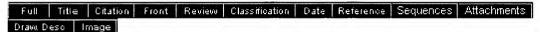
Sep 2, 1997

US-PAT-NO: 5664134

DOCUMENT-IDENTIFIER: US 5664134 A

TITLE: Data processor for performing a comparison instruction using selective enablement and wired boolean logic

oremene and writed boorean rogre



OMC:

☐ 31. Document ID: US 5600846 A

L6: Entry 31 of 39

File: USPT

Feb 4, 1997

US-PAT-NO: 5600846

DOCUMENT-IDENTIFIER: US 5600846 A

TITLE: Data processing system and method thereof

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Draw Desc Image

KMIC

☐ 32. Document ID: US 5598571 A

L6: Entry 32 of 39

File: USPT

Jan 28, 1997

US-PAT-NO: 5598571

DOCUMENT-IDENTIFIER: US 5598571 A

TITLE: Data processor for conditionally modifying extension bits in response to data processing instruction execution

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Drawl Descriptings

KOMC

☐ 33. Document ID: US 5572689 A

L6: Entry 33 of 39

File: USPT

Nov 5, 1996

Sep 24, 1996

US-PAT-NO: 5572689

DOCUMENT-IDENTIFIER: US 5572689 A

TITLE: Data processing system and method thereof

Full Title Citation Front Review Classification Date Reference Sequences Attachments Drawt Desc | Image 34. Document ID: US 5559973 A

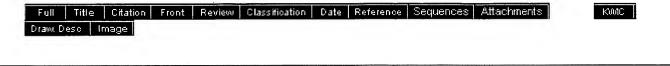
File: USPT

US-PAT-NO: 5559973

DOCUMENT-IDENTIFIER: US 5559973 A

L6: Entry 34 of 39

TITLE: Data processing system and method thereof



☐ 35. Document ID: US 5548768 A

L6: Entry 35 of 39

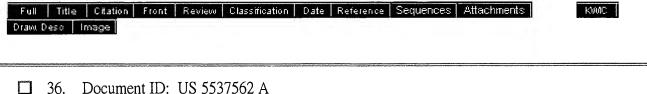
File: USPT

Aug 20, 1996

US-PAT-NO: 5548768

DOCUMENT-IDENTIFIER: US 5548768 A

TITLE: Data processing system and method thereof



L6: Entry 36 of 39

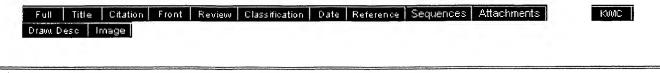
File: USPT

Jul 16, 1996

US-PAT-NO: 5537562

DOCUMENT-IDENTIFIER: US 5537562 A

TITLE: Data processing system and method thereof



☐ 37. Document ID: US 5487022 A

L6: Entry 37 of 39

File: USPT

Jan 23, 1996

US-PAT-NO: 5487022

DOCUMENT-IDENTIFIER: US 5487022 A

TITLE: Normalization method for floating point numbers

☐ 38. Document ID: US 49	945479 A	
L6: Entry 38 of 39	File: USPT	Jul 31, 1990
-PAT-NO: 4945479 CUMENT-IDENTIFIER: US 49454	79 A ific processing system	

☐ 39. Document ID: US 4873630 A

L6: Entry 39 of 39

File: USPT

Oct 10, 1989

US-PAT-NO: 4873630

DOCUMENT-IDENTIFIER: US 4873630 A

TITLE: Scientific processor to support a host processor referencing common memory



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Term	Documents
ALIGN\$6	0
ALIGN.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	227813
ALIGNA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	4
ALIGNABAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABEL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABIE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABILTY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13259
ALIGNABLITY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABLY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	248
(L4 AND ALIGN\$6).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	39

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Term	Documents
ALIGN\$6	0
ALIGN.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	227813
ALIGNA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	4
ALIGNABAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABEL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABIE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABILTY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13259
ALIGNABLITY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABLY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	248
(L4 AND ALIGN\$6).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	39

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DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR				
<u>L6</u>	L4 and align\$6	39	<u>L6</u>	
<u>L5</u>	L3 and target near1 vector\$	3	<u>L5</u>	
<u>L4</u>	L3 and target	60	<u>L4</u>	
<u>L3</u>	(simd or single near1 data) and vector\$ near6 (destination or source) near5 register\$	98	<u>L3</u>	
<u>L2</u>	(simd or single near1 data) and vector\$ near6 (destination or source)	316	<u>L2</u>	
DB=USPT; PLUR=YES; OP=OR				
<u>L1</u>	5671401.pn. or 6035316.pn.	2	<u>L1</u>	

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O- Basic O- Advanced	[Abstract] [PDF Full-Text	(80 KB)] CNF
Member Services  - Join IEEE - Establish IEEE Web Account - Print Format	language 8 1/2 De Vito, D.; Michel, O. EUROMICRO 96. Beyond 2	eneration for the high-level declarative data- 2000: Hardware and Software Design Strategies., UROMICRO Conference, 1995
	[Abstract] [PDF Full-Text	(436 KB)] CNF

## **3 Data-parallel functional programming**

VanderWiel, S.; Davis, J.A.

Parallel Processing Symposium, 1993., Proceedings of Seventh International Page(s): 602 -608

#### [Abstract] [PDF Full-Text (560 KB)] CNF

4 A compiler for a massively parallel distributed memory MIMD compabbot, G.

Frontiers of Massively Parallel Computation, 1992., Fourth Symposium on th Page(s): 12 -20

[Abstract] [PDF Full-Text (840 KB)] CNF



#### 5 Connection Machine model CM-5 system overview

Palmer, J.; Steele, G.L., Jr.

Frontiers of Massively Parallel Computation, 1992., Fourth Symposium on th Page(s): 474 -483

#### [Abstract] [PDF Full-Text (836 KB)] CNF

#### 6 Algorithmic mapping of neural network models onto parallel SIMD

Lin, W.-M.; Prasanna, V.K.; Przytula, K.W.

Computers, IEEE Transactions on , Volume: 40 Issue: 12 , Dec. 1991

Page(s): 1390 -1401

#### [Abstract] [PDF Full-Text (980 KB)] JNL

### 7 Experience with moment-method software of vector processors

Excell, P.S.; Armanious, A.F.

Large Scale Programs for Use in Antenna Performance Prediction, IEE Colloq

1989

Page(s): 3/1 -3/4

#### [Abstract] [PDF Full-Text (188 KB)] CNF

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